

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				Docket Number (Optional) 14993.13		Application Number 10/615,440	
				Applicant(s) Joseph Walsh et al.			
				Filing Date July 7, 2003		Group Art Unit 2817	

U.S. PATENT DOCUMENTS							
EXAMINER	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

U.S. PATENT APPLICATION PUBLICATIONS							
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS								
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS			<i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>
	1	ISBN 0-7803-3416-7 Baker, R. Jacob CMOS circuit design, layout, and simulation / R. Jacob Baker, Harry W. Li, and David E. Boyce (IEEE Press series on microelectronic systems) 1997, page 643.	

EXAMINER S J MOTTOLA	DATE CONSIDERED 10-18-04
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